

AMENDMENT TO THE SPECIFICATION:

Please amend paragraph 55 of the specification as follows:

In the present embodiment, the interrelated control signals 525, 545, 565, and 585, may depend on one another to transition the interpolated clock signal with a substantially analog transition. For example, phase control circuit 510 may receive a charge signal and phase control circuit 530 may receive a discharge signal at substantially the same time so charge circuitry 512 of phase control circuit 510 may be adding charge to charge storage circuitry 516 to increase the amplitude of the interrelated control signal 525 and discharge circuitry 534 may discharge the charge stored in charge storage circuitry 536 at a substantially equivalent rate of discharge as charge storage circuitry 516 accepts the charge from charge circuitry 512. ~~Thus, the interrelated control signals 525 and 545 may comprise ramping amplitudes with substantially equivalent slopes.~~ In particular, charge storage circuitry 516 may have an initial charge at a low amplitude boundary and may rise to a high amplitude boundary at substantially the same rate that the charge level on charge storage circuitry 536 decreases from a high amplitude boundary to a low amplitude boundary....